IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Keiichiro KATA et al.

Title:

PROCESS FOR MANUFACTURING SEMICONDUCTOR DEVICE

AND SEMICONDUCTOR WAFER

Appl. No.:

10/645,782

Filing Date:

8/22/2003

Examiner:

Prenty, Mark V.

Art Unit:

2822

Confirmation No.:

8077

NOTICE OF APPEAL FROM THE EXAMINER TO THE BOARD OF PATENT APPEALS AND INTERFERENCES

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicant hereby appeals to the Board of Patent Appeals and Interferences from the decision of the Examiner in the Final Office Action dated May 23, 2006, finally rejecting Claims 3-12, 15, 16 and 25-32.

- Applicant claims small entity status. []
- Applicant hereby petitions for an extension of time under 37 C.F.R. §1.136(a) for the total number of months checked below:
- [X] Notice of Appeal Fee
 - [X] To be paid as detailed below
 - Not required (Fee paid in prior appeal)

The required fees are calculated below:

	3 18	11/24/2006 TBESHAH1 0	
[]	/24/2006 FC:1253	Small Entity Fees Apply (subtract ½ of above):	\$0.00
		FEE TOTAL:	\$1,520.00
]	11	Extension:	\$0.00
X]		Extension for response filed within the third month:	\$1,020.00
X]		Notice of Appeal Fee	\$500.00

int form in the amount of \$1,520.00 is enclosed.

The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 C.F.R. §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 19-0741. Should no proper payment be enclosed herewith, as by the credit card payment form being unsigned, providing incorrect information resulting in a rejected credit card transaction, or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 1980741.

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Please direct all correspondence to the undersigned attorney or agent at the address indicated below.

Respectfully submitted,

Date <u>November 22, 2006</u>

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